

## This is it... Closing Session

23th Reconfigurable Architectures Workshop  
May 23-24, 2016, Chicago, IL, USA

# RAW 2016



It was great meeting you all!







**I think we had  
some good time together**

## Thank you!

- Authors and attendees
- Program Committee
- The IPDPS Organizing Committee
- If you have any questions or remarks
  - R. Vaidyanathan      [vaidy@lsu.edu](mailto:vaidy@lsu.edu)
  - Juergen Becker      [becker@kit.edu](mailto:becker@kit.edu)
  - Marco Santambrogio      [marco.santambrogio@polimi.it](mailto:marco.santambrogio@polimi.it)

## Looking Ahead... RAW 2017

- What we are going to have!
  - Technical sessions and presentations
    - Only full and short papers
  - Keynotes
  - Panel discussion
  - Interactive sessions (demos)
  - Several Awards
  - Talks' presentations available online



## Our Program Chairs



**Diana Goehringer**

- Ruhr-University Bochum  
- Germany



**Donatella Sciuto**

Politecnico di Milano -  
Italy -

## 2017: what we are willing to...

- RAW format
  - Keeping the to the 2-day format

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- A RAW *social event* on the evening of the 1<sup>st</sup> day!

## 2017: what we are willing to...



Remember!  
Willing to!!!

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- Create a community active through the entire year!



# 2017: what we are willing to...

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  - Keeping the to the 2-day format
- Recording the talks (at least the Keynotes)
- Not only interactive sessions, but a demos one...
  - ... Remember the best demo award!
- A RAW *social event* on the evening of the 1<sup>st</sup> day!
- Create a community active through the entire year!
  - ... Share our research with the community via...

... our website!



The screenshot shows the website for the 23rd Reconfigurable Architectures Workshop (RAW 2016). At the top, there are logos for the IEEE Computer Society and IPDPS 2016 Chicago. Below the logos is a navigation menu with items: Advance program, Topics of Interest, Paper Submission, Publication, Important Dates, Organization, Program Committee, and Workshop editions. The main content area features three images: a cityscape with the Cloud Gate sculpture, a large 'CHICAGO' sign, and a view of a lake. Below the images, the text reads: '23rd Reconfigurable Architectures Workshop', 'Official website: raw.necst.it', and 'Monday and Tuesday, May 23-24, 2016. Chicago Hyatt Regency, Chicago, Illinois USA'. A paragraph of text describes the workshop, mentioning its association with the 30th Annual IEEE International Parallel & Distributed Processing Symposium (IPDPS 2016) and its sponsorship by the IEEE Computer Society and the Technical Committee on Parallel Processing.

<http://raw.necst.it>

## ... our Facebook group

- We do really want to build the RAW community!
- Keep in touch with you all
- Listen to all you feedbacks, suggestions, ideas to improve the workshop

<https://www.facebook.com/groups/ReconfigurableArchitecturesWorkshop/>



... and something more...

- We are going to open a YouTube channel
  - To share our research/demos videos
  - RAW keynotes and talks
- Create a Twitter account
  - To follow our friends during the year
  - Use the #RecArchWorkshop to be connected



See you all next year!

Orlando, FL, USA

May, 2017

# RAW 2017







**BAZINGA**

**SOMEBODY**

**GIVE THAT MAN AN AWARD**

# Best Paper Award: Candidates

- Clustering and Mapping Algorithm for Application Distribution on a Scalable FPGA Cluster
  - Lester Kalms and Diana Göhringer
- A Fast and Accurate Cost Model for FPGA Design Space Exploration in HPC Applications
  - Syed Waqar Nabi and Wim Vanderbauwhede
- A 16-bit reconfigurable encryption processor for Pi-Cipher
  - Mohamed El-Hadedy, Hristina Mihajloska, Danilo Gligoroski, Amit Kulkarni, Dirk Stroobandt and Kevin Skadron
- A Reconfigurable Fixed-Point Architecture for Adaptive Beamforming
  - Daniel Llamocca and Daniel Aloï

# Best Paper Award: Winners

- Clustering and Mapping Algorithm for Application Distribution on a Scalable FPGA Cluster
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- A 16-bit reconfigurable encryption processor for Pi-Cipher
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# Best Demo Award: Candidates

- Parameterizable FPGA-based Kalman Filter Coprocessor Using Piecewise Affine Modeling
  - Aaron Mills, Phillip Jones and Joseph Zambreno
- Resource-Efficient Scheduling for Partially-Reconfigurable FPGA-based Systems
  - Andrea Purgato, Davide Tantillo, Marco Rabozzi, Donatella Sciuto and Marco D. Santambrogio
- On the Automation of High Level Synthesis of Convolutional Neural Networks
  - Emanuele Del Sozzo, Andrea Solazzo, Antonio Rosario Miele and Marco D. Santambrogio



# Best Demo Award: Winner

- On the Automation of High Level Synthesis of Convolutional Neural Networks
  - Emanuele Del Sozzo, Andrea Solazzo, Antonio Rosario Miele and Marco D. Santambrogio

# Best Poster Award: Candidates

- ARTNoCs: An Evaluation Framework for Hardware Architectures of Real-Time NoCs
  - Salma Hesham, Diana Goehringer and Mohamed Abd El Ghany
- Hardware Architectures for Frequent Itemset Mining Based on Equivalence Classes Partitioning
  - Martin Letras, Raudel Hernández-León and Rene Cumplido
- On How to Improve FPGA-Based Systems Design Productivity via SDAccel
  - Gianluca Durelli, Marco D. Santambrogio, Michaela Blott, Lorenzo Di Tucci, Enrico Reggiani and Giulia Guidi
- Enabling Dynamic Reconfiguration of Numerical Methods for the Robotic Motion Control Task
  - Fynn Schwiegelshohn, Florian Kaestner and Michael Huebner

## Best Poster Award: Winner

- Enabling Dynamic Reconfiguration of Numerical Methods for the Robotic Motion Control Task
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## Feedback time!



**WHO,  
ME?**



## Feedback time!



Together we can make  
a better RAW!



See you all next year!

Orlando, FL, USA

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